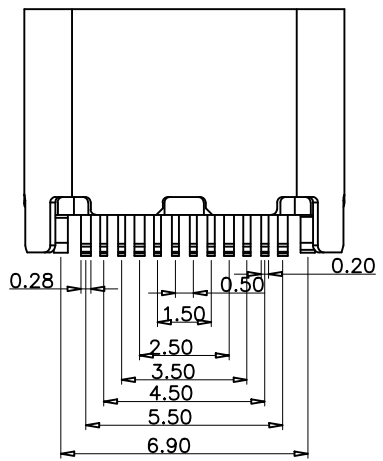
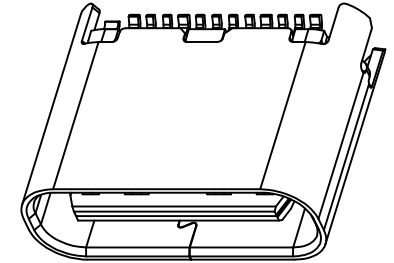
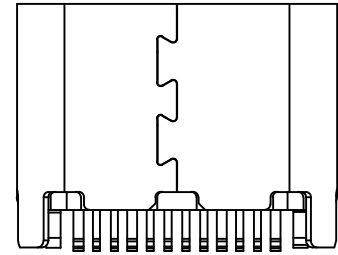
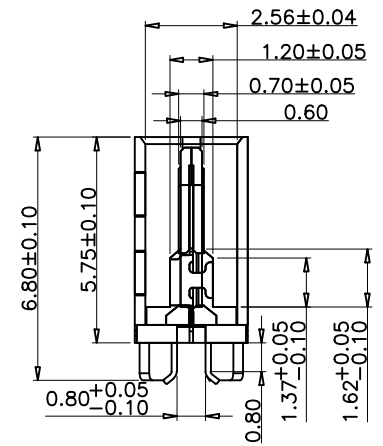
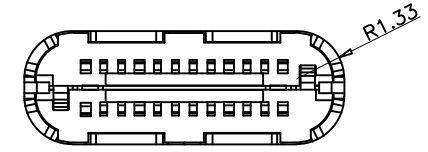


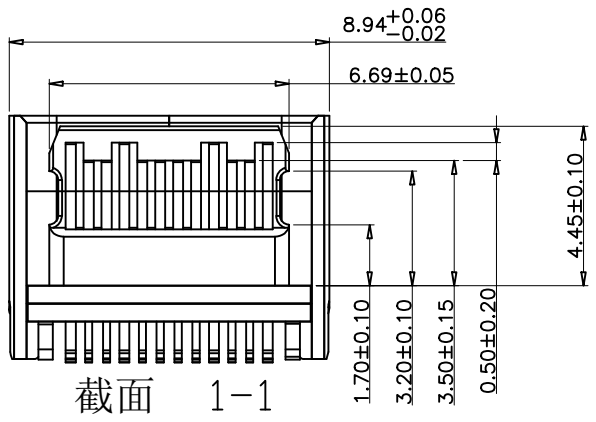
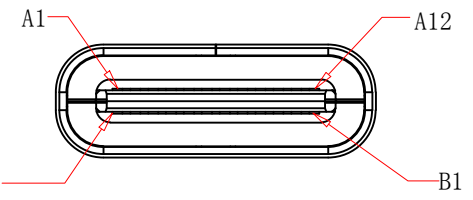


RoHS Compliant

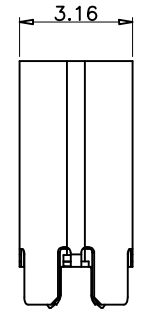
REV.	ZONE	DESCRIPTION	DESIGN	DATE
B		NEW		



截面 A-A



截面 1-1



- NOTES: 1.MATERIAL:
MOLDING: LCP UL94 V-0
CONTACT: COPPER ALLOY.
GOLD PLATED 1u" Min ON CONTACT AREA,
GOLD PLATED 1u" Min ON SOLDER AREA,
SHELL: SUS304-H,T=0.30±0.03mm
50u" NICKEL PLATING OVER ALL.
SHILD: SUS304-H,T=0.20±0.03mm
- 2.MECHANICAL:
INSERTION: 5~20N.
EXTRACTION: 8~20N AFTER TEST.
DURABILITY: 10000 CYCLES
SHELL SALT FOG FOR 48 H
- 3.ELECTRICAL:
CURRENT: 5A FOR VBUS;
1.25A FOR GND PIN.
0.25A FOR OTHER.
VOLTAGE: 20 V MAX
WITHSTANDING VOLTAGE: 100V AC R.M.S.
CONTACT RESISTANCE: 40mΩ MAX.
INSULATION RESISTANCE: 100MΩ MIN.
- 4.ENVIRONMENTAL
TEMPERATURE RANGE -25°C ~ +85°C

X.	±0.50	X.°	± 2°	UNITS mm
.X	±0.30	.X°	± 1°	
.XX	±0.10	.XX°	± 0.5°	MAT'L
.XXX	± 0.05			
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				Q'TY

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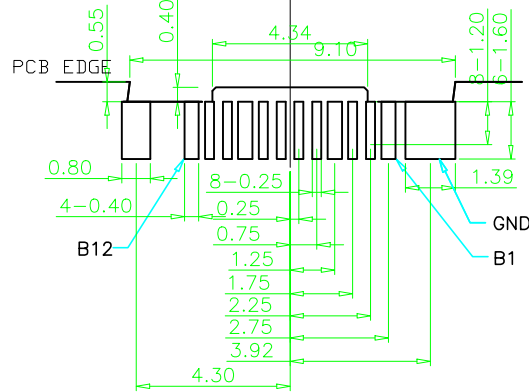
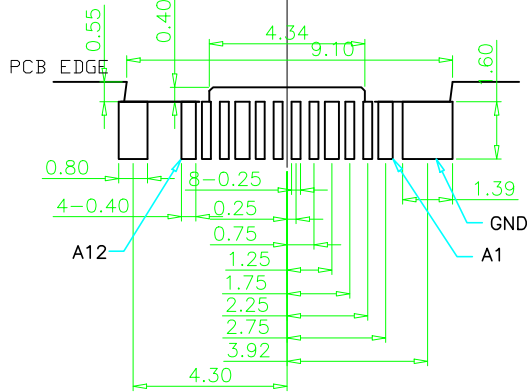
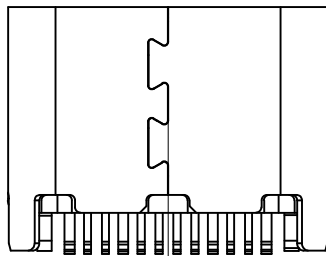
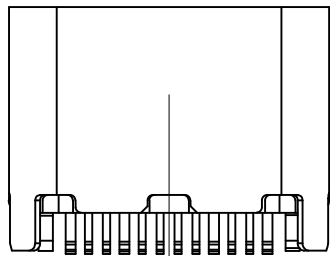
TITLE: USB 3.1type-c 24P夹板短体5.7

APPD: DWG NO.: CT-CTF-Q24A-A570

SCALE	SHEET	REV.
1/1	1/1	A1



REV.	ZONE	DESCRIPTION	DESIGN	DATE
B		NEW		



RECOMMENDED P.C.B. LAYOUT (T:0.80mm)
TOLERANCE UNSPECIFIED ±0.05mm

USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS

Signal Name	Description	PIN	Signal Name	Description
GND	Ground return	B12	GND	Ground return
SSTXp1	Positive half of first SuperSpeed TX differential pair	B11	SSRXp1	Positive half of first SuperSpeed RX differential pair
SSTXn1	Negative half of first SuperSpeed TX differential pair	B10	SSRXn1	Negative half of first SuperSpeed RX differential pair
VBUS	Bus Power	B9	VBUS	Bus Power
CC1	Configuration Channel	B8	SBU2	Sideband Use (SBU)
Dp1	Positive half of the USB 2.0 differential pair-Position 1	B7	Dn2	Negative half of the USB 2.0 differential pair-Position 2
Dn1	Negative half of the USB 2.0 differential pair-Position 1	B6	Dp2	Positive half of the USB 2.0 differential pair-Position 2
SBU1	Sideband Use (SBU)	B5	CC2	Configuraation Channel
VBUS	Bus Power	B4	VBUS	Bus Power
SSRXn2	Negative half of second SuperSpeed RX differential pair	B3	SSTXn2	Negative half of second SuperSpeed TX differential pair
SSRXp2	Positive half of second SuperSpeed RX differential pair	B2	SSTXp2	Positive half of second SuperSpeed TX differential pair
A12	GND	B1	GND	Ground return

X. ±0.50	X.° ± 2°	UNITS mm	深圳市创粤科技有限公司			
.X ±0.30	.X° ± 1°					
.XX ±0.10	.XX° ± 0.5°	MAT'L /	TITLE: USB 3.1type-c 24P夹板短体5.7			
.XXX ± 0.05						
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			Q'TY /			CHKD:
				DR:		